# Vision

Semiconductor ASICs will have optimized design with zero tolerance for bugs and errors

# Mission

Certificate holders shall be "Globally Competitive World-class Engineers" ready to develop IPs, ASICs and SOCs

# SILICON INSTITUTE OF TECHNOLOGY
is India' only VLSI Design Training Program with

- Stipend paid Training Period (Collaterals apply)
- Job Guaranteed with *Silicon Interfaces*®!

# Features

- Promoted by Silicon Interfaces with 30+ years in VLSI Industry
- The Institute is an *in-person* Industry-driven Training Programs
- Close liaison with top Academia in India
- **Faculty** includes, Internationally Qualified, Trained and Experienced in Semiconductor Professors and Industry Professionals

# Program

- **6-9 month** Advanced VLSI Design program

- Program includes **Curriculum** (designed in conjunction with *top Semiconductor companies* and their requirements) with following modules:

    – Digital Logic Design (Combinational, Sequential, State Machines and Timing)

    – EDA Tool Flow

    – Linux, Shell Scripting for Automation

    – Design RTL – Verilog

    – Object Oriented Programming Systems (OOPS)

    – RTL & VDL – SystemVerilog

    – Verification Methodologies – UVM

    – Protocols – Interconnect IP (PCIe & AXI)

    – Python

    – Hands-on Projects,  online/in-person Seminars, Paper Writing

# Program

- **Host & Interconnect**

  – Generic Processor
  – PCIe
  – ARM® AMBA AXI

- **Verticals**

  – Data Communications (SPI & I2C)
  – Wired & Wireless Networking (Optional)

- **Laterals** (Optional)

  – Portable Stimulus
  – Low Power
  – CDC
  – Fault Simulations with AI/ML
  – Emulations

*Silicon Interfaces®* & SILICON INSTITUTE OF TECHNOLOGY (SIT) reserves the rights to change program without notice and the Company decision shall be binding.

Building Careers. Building Chips.™

# Program

- • Periodically, there are Tests, Check Points & Counseling

- • Certification Examination shall be conducted

- • State-of-the-art training facilities with hardware and software equipment and Tier 1 EDA tools

- • A mix of Theory + Lab projects makes the trainees understand concepts and then apply.

- • Course materials created based on real-world experience + Research in US universities.

- • Overlay practical Training imparted in conjunction with Industry

- • Papers submitted to VLSI Design Conferences, like DAC USA, DATE Europe, DVCON Worldwide, Etc. Selected Paper Authors are sponsored for in-person presentations in Europe & USA

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# Eligibility Criteria

- B.Tech/BE/BSc. Engg (Graduation)
- M.Tech/ME/MSc. Engg (post Graduation)
  - Electronics Engineering
  - Electronics & Communications Engineering
  - and related fields)

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# Selection Process

- Presentation On Program.

- Test on Aptitude (Problem Solving & Analytical)

- Test on Technical (Logic Design)

- Technical Interview

- Communications Skills

- General Interview.

# Off to a great career!!!

- Silicon Interfaces has Tier 1 Customer in
  - North America
  - Europe
  - Asia Pacific
- Silicon Interfaces vested Partners are top EDA Companies
- SIT Alumni leads the Global Semiconductor industry

# Be all that you can be.

# Join SIT – now!!!